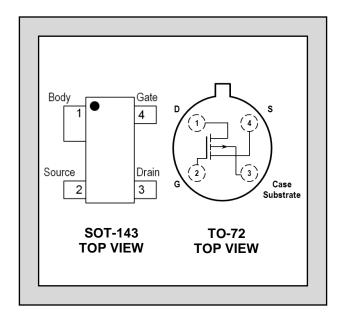


### Twenty-Five Years Of Quality Through Innovation

## 3N163, 3N164

## P-CHANNEL ENHANCEMENT MODE MOSFET

FEATURES					
VERY HIGH INPUT IMPEDANCE					
HIGH GATE BREAKDOWN					
ULTRA LOW LEAKAGE					
FAST SWITCHING					
LOW CAPACITANCE					
ABSOLUTE MAXIMUM RATINGS					
@ 25°C (unless otherwise stated)					
Drain-Source or Drain-Gate Voltage					
3N163	-40V				
3N164	-30V				
Drain Current	50mA				
Storage Temperature	-55°C to +150°C				
Power Dissipation TO-72 case	375mW²				
Power Dissipation SOT-143 case	350mW <sup>3</sup>				

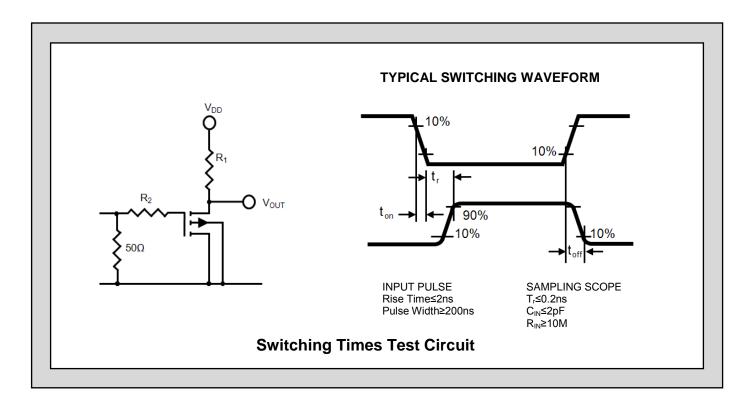


#### ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTI	ERISTIC	3N1	3N163 3N164		UNITS	CONDITIONS		
			MIN	MAX	MIN	MAX			
Igss	Gate Leakage Currer	nt		-10		-10	pА	V <sub>GS</sub> =-40V,	V <sub>DS</sub> =0 (3N163), V <sub>SB</sub> =0V
		T <sub>A</sub> =+125°C		-25		-25		V <sub>GS</sub> =-30V,	V <sub>DS</sub> =0 (3N164), V <sub>SB</sub> =0V
BV <sub>DSS</sub>	Drain-Source Breakd	own Voltage	-40		-30			I <sub>D</sub> =-10μA	V <sub>GS</sub> =0, V <sub>BS</sub> =0
BV <sub>SDS</sub>	Source-Drain Breakd	own Voltage	-40		-30		V	Is=-10μA	V <sub>GD</sub> =0, V <sub>BD</sub> =0
$V_{GS(th)}$	Threshold Voltage		-2.0	-5.0	-2.0	-5.0		$V_{DS}=V_{GS}$	$I_D$ =-10 $\mu$ A, $V_{SB}$ =0 $V$
V <sub>G</sub> S	Gate Source Voltage	(on)	-3.0	-6.5	-3.0	-6.5		V <sub>DS</sub> =-15V	I <sub>D</sub> =-0.5mA, V <sub>SB</sub> =0V
IDSS	Zero Gate Voltage, D	rain Current (off)		-200		-400	pА	V <sub>DS</sub> =-15V	V <sub>GS</sub> =0, V <sub>SB</sub> =0V
I <sub>SDS</sub>	Zero Gate Voltage, S	ource Current		-400		-800		V <sub>SD</sub> =-15V	$V_{GS}=0, V_{DB}=0V$
R <sub>DS(on)</sub>	Drain-Source on Res	istance		250		300	ohms	V <sub>GS</sub> =-20V	I <sub>D</sub> =-100μA, V <sub>SB</sub> =0V
I <sub>D(on)</sub>	On Drain Current		-5.0	-30	-3.0	-30	mA	V <sub>DS</sub> =-15V	V <sub>GS</sub> =-10V, V <sub>SB</sub> =0V
<b>g</b> fs	Forward Transcondu	ctance	2.0	4.0	1.0	4.0	mS	V <sub>DS</sub> =-15V	I <sub>D</sub> =-10mA f=1kHz
<b>g</b> og	Output Admittance			250		250	μS		
Ciss	Input Capacitance-O	utput Shorted		3.5		3.5	pF	V <sub>DS</sub> =-15V	I <sub>D</sub> =-10mA <sup>1</sup> f=1MHz
Crss	Reverse Transfer Ca	pacitance		0.7		0.7			
Coss	Output Capacitance I	nput Shorted		3.0		3.0			

#### SWITCHING CHARACTERISTICS T<sub>A</sub>=25°C and V<sub>BS</sub>=0 (unless otherwise noted)

SYMBOL	CHARACTERISTIC	3N163		3N164		UNITS	CONDITIONS
		MIN	MAX	MIN	MAX		
ton	Turn-On Delay Time		12		12	ns	V <sub>DD</sub> =-15V, V <sub>SB</sub> =0V
tr	Rise Time		24		24		I <sub>D(on)</sub> =-10mA <sup>1</sup>
t <sub>off</sub>	Turn-Off Time		50		50		$R_G=R_L=1.4K$



#### **NOTES:**

- 1. For design reference only, not 100% tested.
- 2. Derate 3mW/°C above 25°C
- 3. Derate 3.5mW/°C above 25°C
- 4. All min/max limits are absolute numbers. Negative signs indicate electrical polarity only.

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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